S ISWARYA

E-mail: : [ishubujji24@gmail.com](mailto:ishubujji24@gmail.com)

Phone: 8888944886

**CAREER OBJECTIVE:**

To be successful person in the field where I am going to work and seeking position to utilize my skills and ability in different fields.

**KEY POINTS:**

Software Testing Skills:

**Manual Testing:**

* Good knowledge of **SOFTWARE DEVELOPMENT LIFE CYCLE (SDLC).**
* Good knowledge of **WHITE BOX TESTING** and **BLACK BOX TESTING**.
* Good knowledge **FUNCTIONAL** and **NON-FUNCTIONAL TESTING.**
* Good exposure on **BUG LIFE CYCLE.**
* Good knowledge of **SOFTWARE TESTING LIFE CYCLE (STLC).**

Technical Skill Set:

Software Testing : Manual Testing

Languages : Core Java

Operating System : Windows7

Achievements:

* I have participated in PAPER PRESENTATION held as a part of "E-SANDHAAN 2K15" at GATE institute of technology and sciences on 31st of march -2015.
* I have organized in the event “POSTER PRESENTATION” held as a part of “E-DAY 2K17” at Ananthalakshmi institute of technology and sciences on 18th&19th of march-2017.

Personal traits:

* Good verbal and written communication skills.
* Willingness to learn, team facilitator and hard worker.

**EDUCATIONAL QUALIFICATIONS:**

Academic Details:

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Qualification** | **Year Of Passing** | **INSTITUTION** | **UNIVERSITY** | **Percentage** |
| B.Tech | 2013 -2017 | AnanthaLaskhmi Institute Of Technology & Sciences,Ananthapur | JNTUA | 68.15 |
| Intermediate | 2010 -2013 | Sri Chaithanya Junior College, Ananthapur | Board of Intermediate Education(A.P) | 84.2 |
| C.B.S.E | 2010 | ST.JosephPublic School,Ananthapur | Central Board Of Secondary Education | 80.0 |

**PROJECTS AND ACHIEVEMENTS:**

Acadcemic Project Information:

**32\*32Bit Multiprecision Razor Based Dynamic Voltage Scaling Multiplier With Operands Scheduler**

Under the guidance of Mr.V.DamodharM.Tech., Assistant Professor,Ananthalakshmi Institute of Technology &Science, Anantapur.

The goal of this project is to determine the optimum voltage and frequency operating conditions for minimum power consumption this low power multiplier is fabricated in AIMS 0.35micrometer technology.Experimental results shows that the proposed MP design features a 28.2% and15.8% reduction in circuit area and power consumption compared with conventional fixed width reduction.When combining this mp design with error tolerant razor based DVS,PPand the proposed novel operands scheduler,77.7%-86.3% total power reduction is achieved with total silicon area ahead as low as 11.1%.

**PERSONAL INFORMATION:**

Personal profile:

* Name : S.Iswarya
* Date of Birth : 24-06-1996
* Gender : Female
* Father name : S Vasanth
* Language known : English, Hindi, and Telugu ,Marathi.
* Hobbies : Cooking,Playing Indoor and Outdoor Games .
* Address : At Post - Moshi Tal -khed Dis-Pune.

**DECLARATION:**

I do hereby declare that all the information given above is true to the best of my knowledge and belief.

Place: Pune

Date:

* (S.Iswarya)